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2782
Berenbaum 9-4-5-5

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

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Case: 9-4-5-5

Serial No.: 09/538,755

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Group: 2782

Examiner: Unassigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

Signature: *Kevin M. Mason* Date: September 21, 2001

Title: Method and Apparatus for Splitting Packets in a Multithreaded VLIW Processor

TRANSMITTAL OF FORMAL DRAWINGS

Assistant Commissioner for Patents
Washington, D.C. 20231

Attention: Official Draftsperson

SIR:

Applicants submit herewith six (6) sheets of formal drawings in the above-referenced application.

Respectfully submitted,

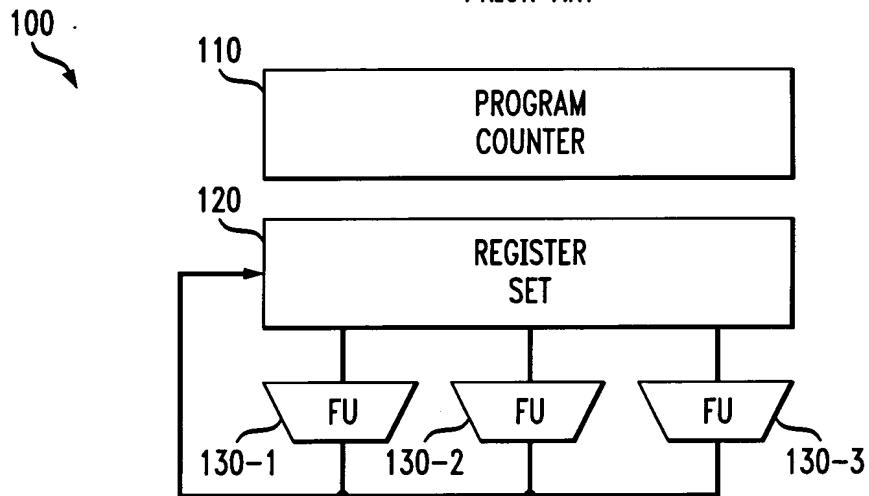
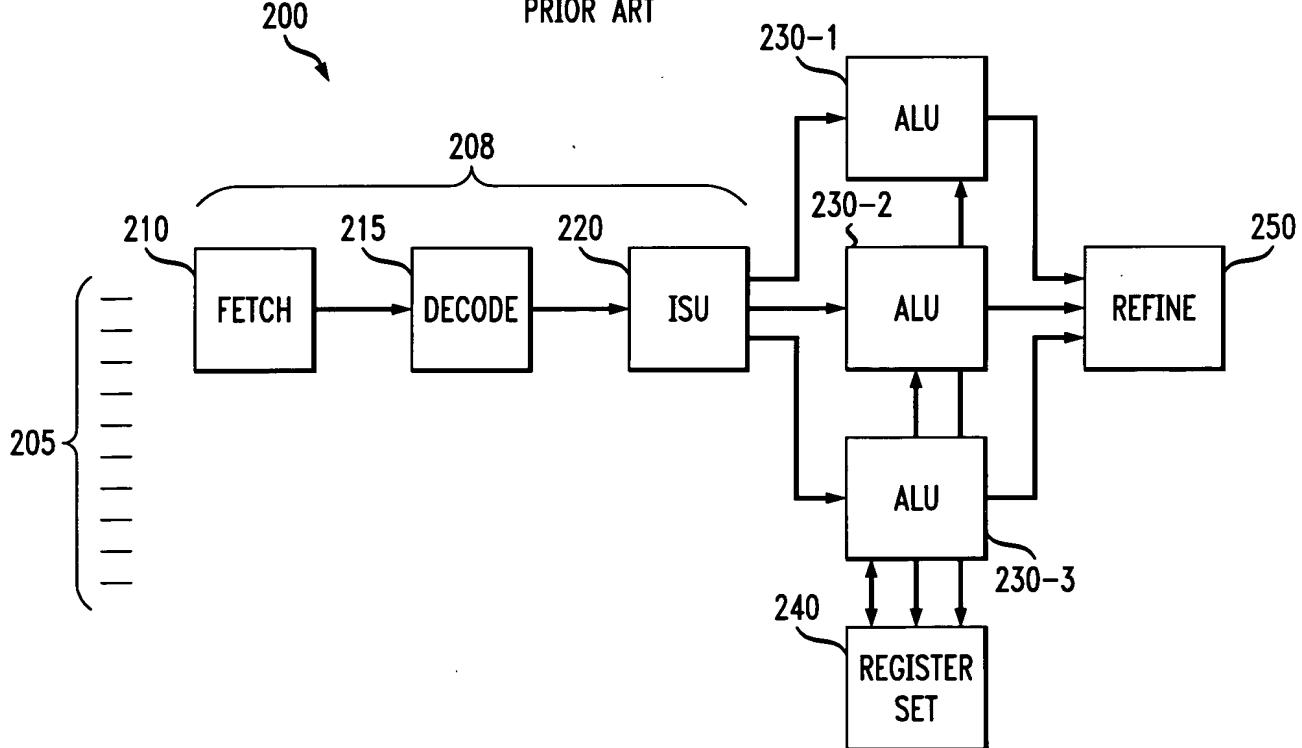
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Date: September 21, 2001

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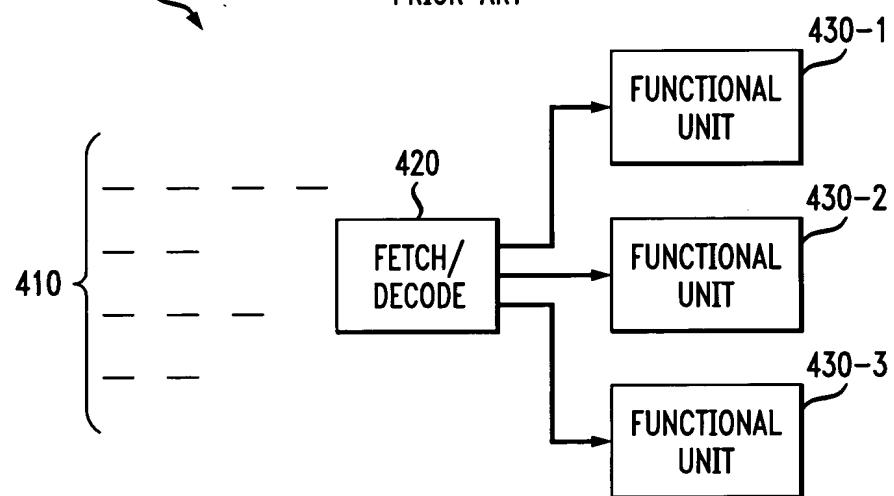
FIG. 1
PRIOR ARTFIG. 2
PRIOR ART7-2
15

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FIG. 3

310 { L3: ADD R0, R1, R2
 L2: SUB R3, R4, R2
 L1: OR R6, R1, R5

FIG. 4
PRIOR ART

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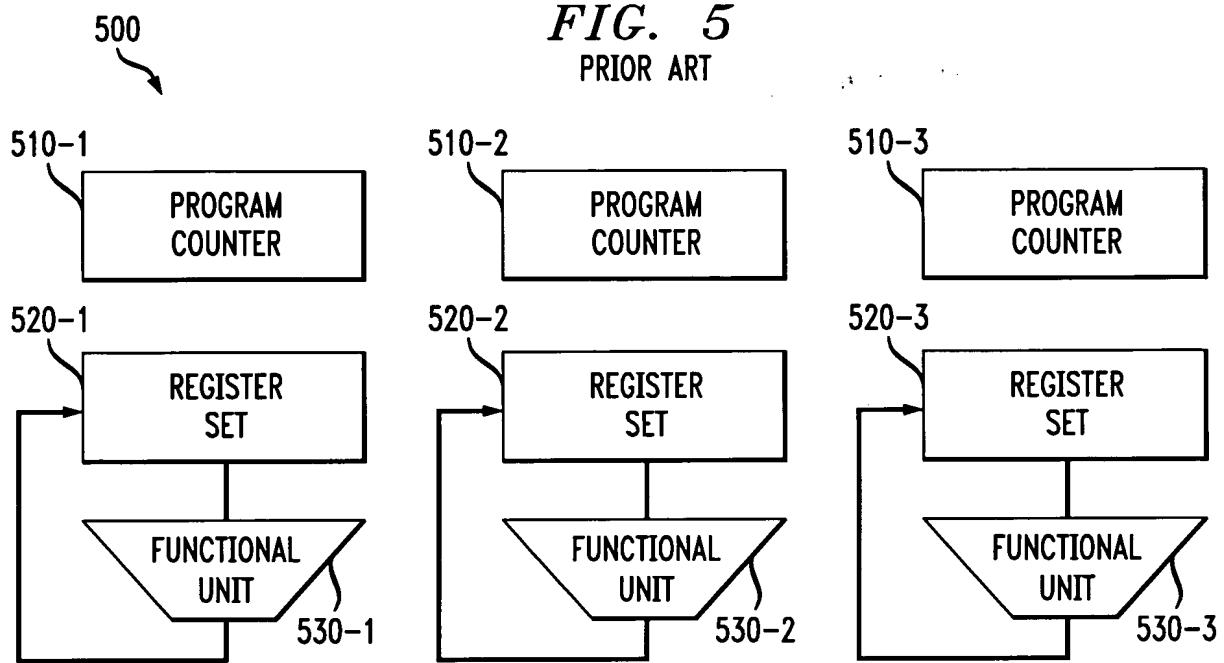
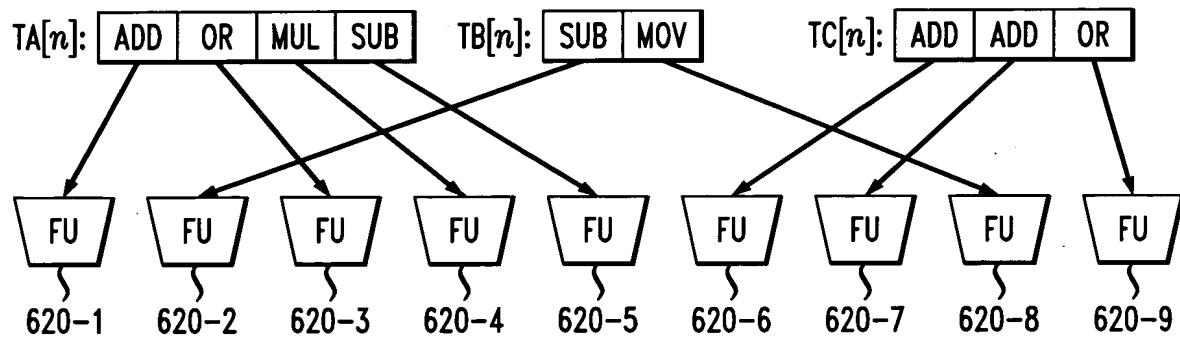
FIG. 5
PRIOR ART

FIG. 6



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FIG. 7A
PRIOR ART

700

710

720

730

FETCH
GET A PACKET (UP
TO K INSTRUCTIONS)
FROM MEMORY

DECODE
DETERMINE FUs AND
REGISTERS FOR UP TO
K INSTRUCTIONS

EXECUTE:
PERFORM
OPERATION AND
PROCESS RESULT

750

FIG. 7B

760

770

780

790

FETCH
GET UP TO N
PACKETS (UP TO K
INSTRUCTIONS)
FROM MEMORY

DECODE
DETERMINE FUs
AND REGISTERS
NEEDED FOR UP TO
N*K INSTRUCTIONS

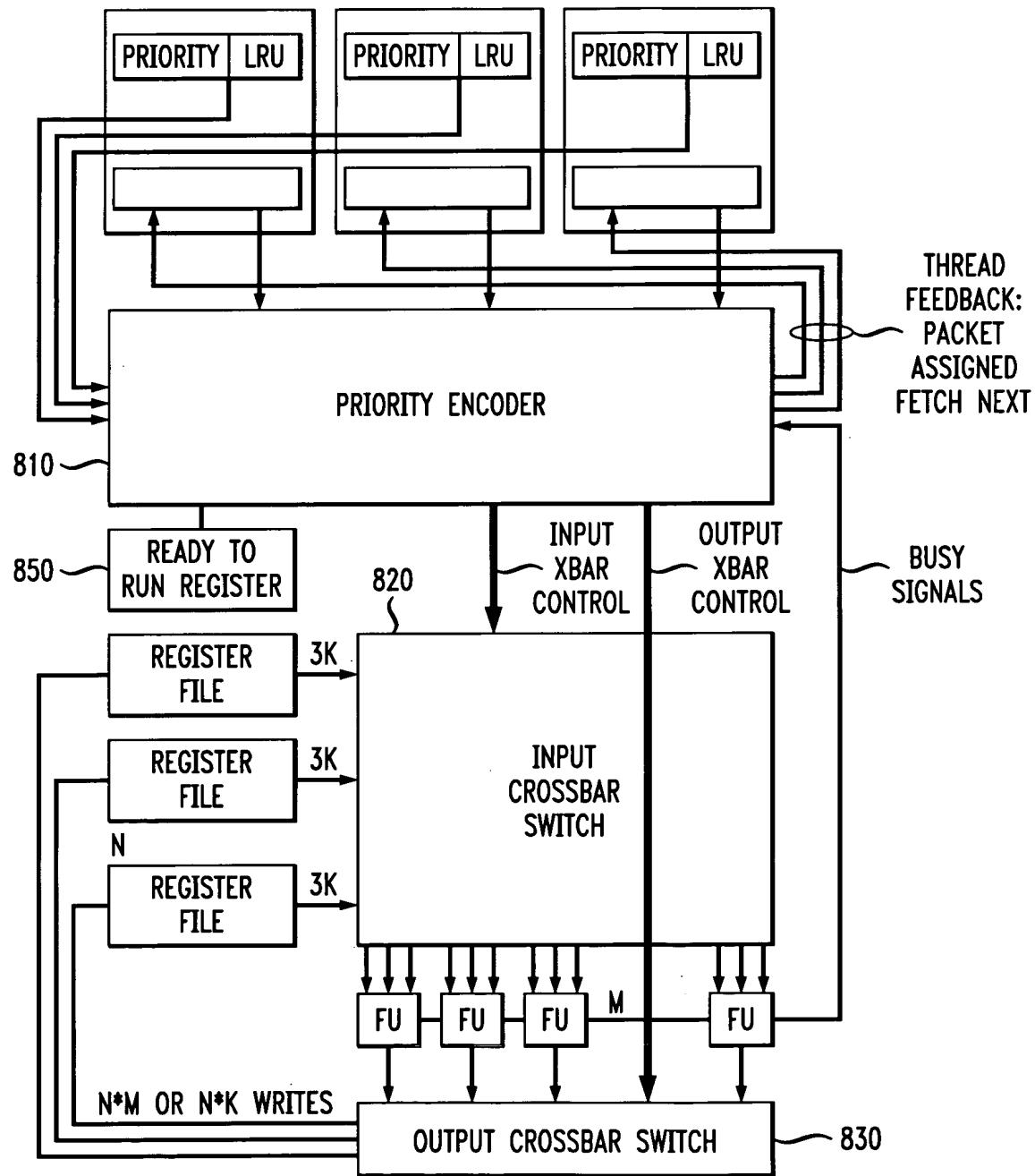
ALLOCATE
SELECT
APPROPRIATE
INSTRUCTIONS AND
ASSIGN THEM TO FUs

EXECUTE:
PERFORM
OPERATION
AND PROCESS
RESULT

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780

FIG. 8



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FIG. 9

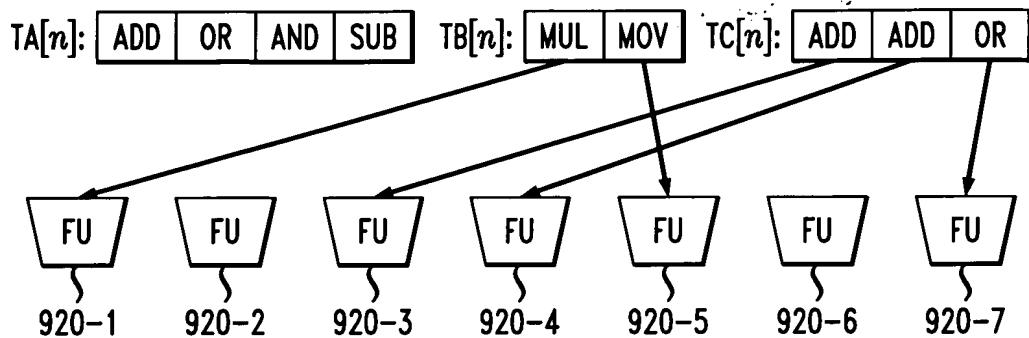


FIG. 10A

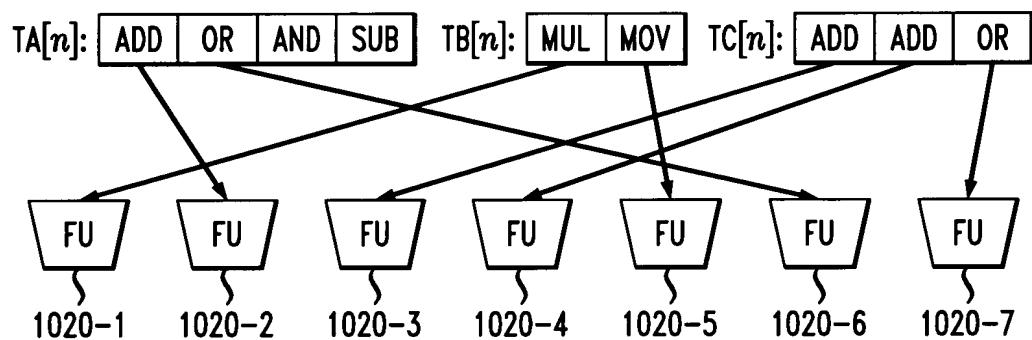


FIG. 10B

